

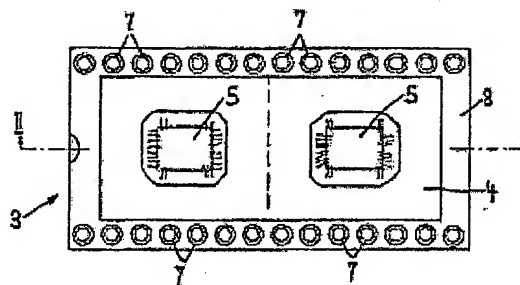
Electronic module for integrated circuit mounting on PCB - has space provided between rows of connector pins for chips mounted on board through which pins pass

Patent number: CH665060
Publication date: 1988-04-15
Inventor: GIGON DICK
Applicant: VALTRONIC S A
Classification:
- international: H01L27/00; H05K1/00; H01R9/00; G11C5/04
- european: G11C5/00, H05K7/10G, H05K1/14B
Application number: CH19850004477 19851016
Priority number(s): CH19850004477 19851016

Abstract of CH665060

A connector (3) has two parallel rows of pins (7) protruding from the base of a pedestal (8) in which a socket is formed for insertion of an integrated circuit (1). The pins (7) pass through a printed circuit board (4) and into a base circuit (2) to which they can be soldered. One or more integrated circuit chips (5) are wire-bonded to the board (4) and encapsulated in resin (6), in the spaces between heads of the pins (7). Overall height of the module does not exceed that of a standard dual-in-line package.

USE/ADVANTAGE - With basic read-only memory or UV EPROM and random-access extension memory chips. Module affords saving of surface area and in certain applications simplification of printed connections.



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